# **Low Noise Transistors**

# **PNP Silicon**

### **Features**

• These are Pb-Free Devices\*

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	-45	Vdc
Collector - Base Voltage	V <sub>CEB</sub>	-50	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	Vdc
Collector Current – Continuous	Ic	-100	mAdc
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above T <sub>A</sub> = 25°C	P <sub>D</sub>	625 5.0	mW mW/°C
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above T <sub>A</sub> = 25°C	P <sub>D</sub>	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

## THERMAL CHARACTERISTICS

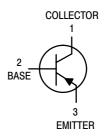
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

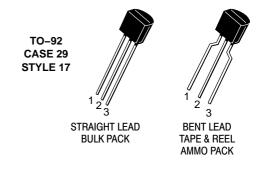
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



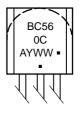
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## **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

## ORDERING INFORMATION

Device	Package	Shipping		
BC560CG	TO-92 (Pb-Free)	5000 Units / Bulk		
BC560CZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack		

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		1			
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = –10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	-45	_	_	Vdc
Collector – Base Breakdown Voltage (I <sub>C</sub> = –10 µAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	-50	_	1	Vdc
Emitter – Base Breakdown Voltage ( $I_E = -10 \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	12	-	-	Vdc
Collector Cutoff Current $(V_{CB} = -30 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -30 \text{ Vdc}, I_E = 0, T_A = +125^{\circ}\text{C})$	I <sub>CBO</sub>	_ _	_ _	-15 -5.0	nAdc μAdc
Emitter Cutoff Current $(V_{EB} = -4.0 \text{ Vdc}, I_C = 0)$	I <sub>EBO</sub>	-	-	-15	nAdc
ON CHARACTERISTICS					
DC Current Gain $ \begin{pmatrix} I_C = -10 \ \mu Adc, \ V_{CE} = -5.0 \ Vdc \end{pmatrix} $ $ \begin{pmatrix} I_C = -2.0 \ mAdc, \ V_{CE} = -5.0 \ Vdc \end{pmatrix} $	h <sub>FE</sub>	100 380	270 500	_ 800	-
	V <sub>CE(sat)</sub>	- - -	-0.075 -0.3 -0.25	-0.25 -0.6 -	Vdc
Base – Emitter Saturation Voltage ( $I_C = -100 \text{ mAdc}$ , $I_B = -5.0 \text{ mAdc}$ )	V <sub>BE(sat)</sub>	_	-1.1	-	Vdc
Base–Emitter On Voltage $ \begin{array}{l} (I_C=-10~\mu Adc,~V_{CE}=-5.0~Vdc)\\ (I_C=-100~\mu Adc,~V_{CE}=-5.0~Vdc)\\ (I_C=-2.0~mAdc,~V_{CE}=-5.0~Vdc) \end{array} $	V <sub>BE(on)</sub>	- - -0.55	-0.52 -0.55 -0.62	- - -0.7	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain – Bandwidth Product (I <sub>C</sub> = -10 mAdc, V <sub>CE</sub> = -5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	-	250	-	MHz
Collector–Base Capacitance $(V_{CB} = -10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C <sub>cbo</sub>	-	2.5	-	pF
Small–Signal Current Gain (I <sub>C</sub> = -2.0 mAdc, V <sub>CE</sub> = -5.0 V, f = 1.0 kHz)	h <sub>fe</sub>	450	600	900	pF
Noise Figure $ \begin{array}{l} \text{Noise Figure} \\ \text{($I_C = -200$ $\mu$Adc, $V_{CE} = -5.0$ Vdc, $R_S = 2.0$ k$\Omega$, $f = 1.0$ kHz)} \\ \text{($I_C = -200$ $\mu$Adc, $V_{CE} = -5.0$ Vdc, $R_S = 100$ k$\Omega$, $f = 1.0$ kHz, $\Delta f = 200$ kHz)} \end{array} $	NF <sub>1</sub> NF <sub>2</sub>	_ _	0.5	2.0 10	dB

I<sub>B</sub> is value for which I<sub>C</sub> = -11 mA at V<sub>CE</sub> = -1.0 V.
 Pulse test = 300 μs – Duty cycle = 2%.

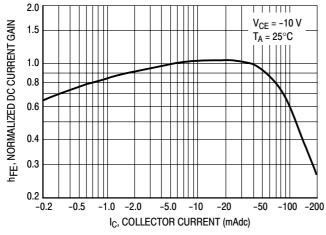


Figure 1. Normalized DC Current Gain

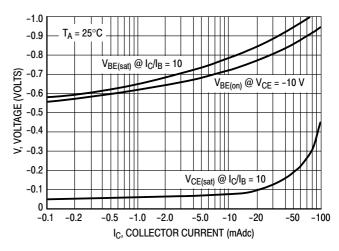


Figure 2. "Saturation" and "On" Voltages

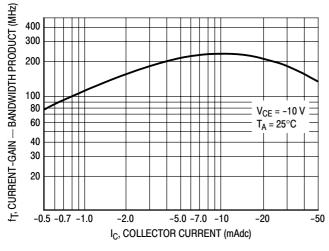


Figure 3. Current-Gain — Bandwidth Product

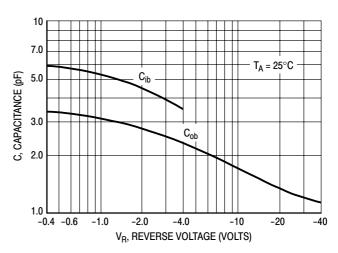


Figure 4. Capacitance

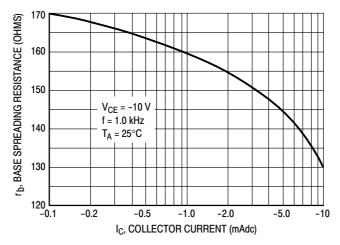
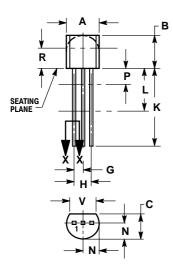


Figure 5. Base Spreading Resistance

#### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AM** 



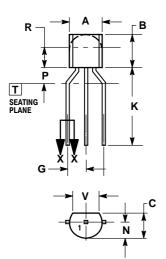
STRAIGHT LEAD **BULK PACK** 



#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	



**BENT LEAD** TAPE & REEL AMMO PACK



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
  CONTOUR OF PACKAGE BEYOND
- DIMENSION R IS UNCONTROLLED
- LEAD DIMENSION IS UNCONTROLLED IN PAND BEYOND DIMENSION K MINIMUM.

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.45	5.20	
В	4.32	5.33	
С	3.18	4.19	
D	0.40	0.54	
G	2.40	2.80	
J	0.39	0.50	
K	12.70		
N	2.04	2.66	
P	1.50	4.00	
R	2.93		
٧	3.43		

STYLE 17:

COLLECTOR PIN 1.

BASE

EMITTER

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